

3-28-03

EAST - [Dwin_Default.wsp:1]

File View Edit Tools Window Help

BRS:

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L1: (1) ("6175946").PN.

L2: (4) (("6175946") or ("6108494") or ("5881270") or ("6339836

L3: (5) (("6175946") or ("6108494") or ("5881270") or ("6339836

L4: (8) (("6175946") or ("6108494") or ("5881270") or ("6339836

L5: (9) (("6175946") or ("6108494") or ("5881270") or ("6339836

L6: (9) (("6175946") or ("6108494") or ("5881270") or ("6339836

L7: (21) (integrated adj circuit) and (distributed adj simulati

	U	1	Document ID	Issue Date	Pages	Titl
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6530054 B2	20030304	20	Method and apparatus for t circuit design
2	<input type="checkbox"/>	<input type="checkbox"/>	US 6526562 B1	20030225	10	Methods for developing an chip design
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6347388 B1	20020212	20	Method and apparatus for t circuit design
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6345242 B1	20020205	18	Synchronization mechanism hardware simulation
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6341364 B1	20020122	8	Computer assisted method c electrical circuit
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6253359 B1	20010626	16	Method for analyzing circv capacitive coupling in dig
7	<input type="checkbox"/>	<input type="checkbox"/>	US 6195628 B1	20010227	15	Waveform manipulation in t
8	<input type="checkbox"/>	<input type="checkbox"/>	US 6182258 B1	20010130	22	Method and apparatus for t circuit design
9	<input type="checkbox"/>	<input type="checkbox"/>	US 6117181 A	20000912	18	Synchronization mechanism hardware simulation
10	<input type="checkbox"/>	<input type="checkbox"/>	US 6050001 A	20000001	11	

Hits

Details

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